



Bhagwan Mahavir Education Foundation

Jagdishpur, Near O.P.Jindal University, Sonipat-131030 (Haryana)
Ph.:-0130-216651,0130-2325601-605,Fax:-0130-2325600,

Dated: 16/01/2019

Notice

All the faculty members of MVSIT, BMCEM & BMIP are requested to attend the STC on 'Artificial Intelligence and Optimization through ICT' by Electrical Engineering, department NITTTR, Chandigarh during **21-01-19 to 25-01-19**. The tentative time table for the course is attached. Registration will be done on the first day. Daily attendance will be taken twice (i.e. Morning and Afternoon). Everybody have to fill up the feedback forms at the end of the course.

This ICT and Contact mode course has been specifically designed for the faculty of Electrical / Electronics / Computer Science/ Instrumentation & Control Engineering departments. You are requested to invite as many faculty/technical staff from nearby Engineering Colleges as possible.

The programme will be designed around the following major topics:

MATLAB and SIMULINK Programming
Type 1 and Type 2 Fuzzy Logic
Neural Network and Deep Learning
ANFIS Toolboxes
GA, PSO and other optimization algorithm
MATLAB optimization toolbox

Venue:- Seminar Hall, MVSIT

If there is any query regarding the notice, feel free to contact:

Mr Ravi Antil, Asst. Professor (ECE)-8950102089

Mr. Amit Lab Tech.(CSE)-9999513813

Copy to the:-

1. Technical Staff (CSE)
2. Director Principals (MVSIT, BMCEM & BMIP)
3. Worthy Registrar(for kind information)
4. Hon'able Chairman(for kind information)